



JC05 Rev 1/01

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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TAKITA et al.

ATTN: Application Branch

Application No.: 09/046,671

Attorney Dkt. No.: 108077-08003

Filed: March 24, 1998

For: SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents  
Washington, D.C. 20231

February 12, 2002

Sir:

A corrected filing receipt is hereby requested.

Please change the Title from --**SEMICONDUCTOR DEVICE HAVING ELECTRICAL DIVIDED SUBSTRATE AND METHOD FOR FABRICATING THE SEMICONDUCTOR DEVICE--** to "**SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME**".

For the convenience of the Patent and Trademark Office, attached is a photocopy of a Corrected filing receipt on which the errors have been noted in red.

Please charge any deficiency or credit any overpayment with respect to this paper to Deposit Account No. 01-2300.

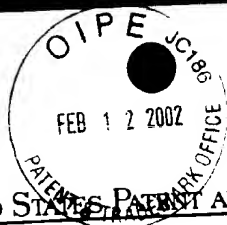
Respectfully submitted,

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## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/046,671	03/24/1998	2823	1356	P8077-8003	29	22	6

CONFIRMATION NO. 2422

## CORRECTED FILING RECEIPT



\*OC00000006441994\*

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1050 CONNECTICUT AVENUE NW SUITE 600  
WASHINGTON, DC 20036-5339

Date Mailed: 08/17/2001

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## Applicant(s)

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## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 09-257085/1997 09/22/1997

If Required, Foreign Filing License Granted 08/17/2001

CPA filed on: 05/14/2001

Projected Publication Date: 11/29/2001

Non-Publication Request: No

Early Publication Request: No

## Title

SEMICONDUCTOR DEVICE HAVING ELECTRICAL DIVIDED SUBSTRATE AND METHOD  
FOR FABRICATING THE SEMICONDUCTOR DEVICE

Preliminary Class



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